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DELHI TECHNOLOGICAL UNIVERSITY



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New Arrivals

Prepared by:-
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S. No	Title	Author	Class No.	Publisher
1	INTRODUCTION TO COMPUTER THEORY	COHEN, DANIEL I.A.	004	WILEY
2	EMBEDDED SYSTEMS : ARCHITECTURE, PROGRAMMING AND DESIGN	KAMAL, RAJ	004.16	MCGRAW HILL
3	AN INTRODUCTION TO THE INTEL FAMILY OF MICROPROCESSORS	ANTONAKOS, JAMES L.	004.165	PEARSON
4	COMPUTER NETWORKS	COMER, DOUGLAS E.	004.6	PEARSON
5	NEURAL NETWORKS	SHANKAR, T.N.	006.32	UNIVERSITY
6	DESIGN OF SYSTEM ON A CHIP : DEVICES AND COMPONENTS	REIS, RICARDO	621.3815	SPRINGER
7	VLSI TECHNOLOGY	SZE, S.M.	621.38173	MCGRAW HILL
8	DIGITAL LOGIC AND COMPUTER DESIGN	MANO, M. MORRIS	621.38195 2	PEARSON
9	ADVANCED DIGITAL COMMUNICATION SYSTEMS	NIIT	621.382	PHI
10	MICROWAVE AND RADAR ENGINEERING	GARG, RAVI	621.384	S.K
11	INTRODUCTION TO CDMA WIRELESS COMMUNICATIONS	ABU-RGHEFF, MOSA ALI	621.3845	ELSEVIER
12	IS-95 CDMA AND CDMA 2000 : CELLULAR/PCS SYSTEMS IMPLEMENTATION	GARG, VIJAY K.	621.3845	PEARSON
13	VHDL : ANALYSIS AND MODELING OF DIGITAL SYSTEMS	NAVABI, ZAINALABEDIN	621.392	MCGRAW-HILL
14	AN INTRODUCTION TO THE DESIGN OF SMALL-SCALE EMBEDDED SYSTEMS	WILMSHURST, TIM	621.392	PALGRAVE
15	VERILOG HDL : A GUIDE TO DIGITAL DESIGN AND SYNTHESIS	PALNITKAR, SAMIR	621.392	PEARSON
16	DIGITAL DESIGN WITH CPLD APPLICATIONS AND VHDL	DUECK, ROBERT K.	621.395	DELMAR
17	BASIC VLSI DESIGN	PUCKNELL, DOUGLAS A.	621.395	PHI
18	VLSI MEMORY CHIP DESIGN	ITOH, KIYOO	621.39732	SPRINGER
