Total No. of Pages 01 THIRD SEMESTER

SUPLEMENTARY EXAMINATION

DIGITAL DESIGN-1 EC203

Roll No. B.Tech(EC) FEB-2019

TIME: 03 Hrs

Maximum Marks:40

Note: Question no 1 is compulsory. Attempt any four from rest questions. Assume suitable missing data if any.

- 1. (a) What is De-Multiplexer? State its use.
 - (b) Simplify the equation $\overline{AC}(\overline{\overline{A}BD}) + \overline{AB}\overline{C}\overline{D} + \overline{A}\overline{(B} + C)$
 - (c) What is T flip-flop? Give the excitation table for T flipflop.
 - (d)Differentiate between Synchronous counter & Asynchronous counter.

 $4 \times 2 = 8$

- (a) What is EX-3 Code? Give the logic circuit for BCD to Ex-3 code 2
 - (b) Implement the logic function $f(ABCD) = \Sigma M(0,1,3,6,7,11,12,13,15)$ $2 \times 4 = 8$ using 8:1 mux.
- (a) What is Gray code ? Give the truth table for converting four bit 3. Binary code to Gray code and obtain the logic expression.

(b) With neat circuit diagram, explain one digit BCD addition.

 $2 \times 4 = 8$

- (a) Design a synchronous counter for counting 7 to 0 and repeat. How it 4. can be converted to an up counter?
 - (b) Discuss the term PAL & PLA.

 $2 \times 4 = 8$

- (a) With neat circuit diagram, explain the working of three bit Flash A/D 5. converter.
 - (b) Give the classification of logic family. Give the characteristics and specifications of TTL& CMOS logic families.

 $2 \times 4 = 8$

- (a) What is EPROM and EEPROM? State their use and also compare them.
 - (b) What is Priority encoder? Design a 2 bit Priority encoder.

 $2 \times 4 = 8$

END