

FEASIBILITY ANALYSIS OF SUPER CONDUCTING FAULT CURRENT LIMITER IN SMART GRID USING SIMULINK

DISSERTATION

SUBMITTED IN PARTIAL FULFILLMENT OF THE REQUIREMENTS
FOR THE AWARD OF THE DEGREE

OF

MASTER OF TECHNOLOGY
IN
POWER SYSTEM

Submitted by:

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CERTIFICATE

I, **Kuldeep Singh Rajput**, Roll No. 2K14/PSY/12 student of **M. Tech. (Power System)**, hereby declare that the dissertation titled "Feasibility Analysis of Super Conducting Fault Current Limiter in Smart Grid Using Simulink" under the supervision of Dr. Narendra Kumar , Professor, Electrical Engineering Department, Delhi Technological University in partial fulfilment of the requirement for the award of the degree of Master of Technology. This dissertation has not been submitted elsewhere for the award of any Degree.

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