

# **DEPARTMENT OF ELECTRICAL ENGINEERING**

## **DELHI TECHNOLOGICAL UNIVERSITY**

(Formerly Delhi College of Engineering)

Bawana Road, Delhi-110042

### **CERTIFICATE**

This is to certify that the Project entitled “**Realization of wave active filter using Voltage Differencing Buffered Amplifier**” submitted by **Sandeep Rana** in completion of major project dissertation for Master of Technology degree in **Control and Instrumentation** at Delhi Technological University is an authentic work carried out by him under my supervision and guidance.

This is to certify that the above statement made by the candidate is correct to the best of my knowledge.

Place: Delhi

**(PROF. PRAGATI KUMAR)**

Professor

Department of Electrical Engineering

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**SANDEEP RANA**

## ABSTRACT

Analog filters play a key role in any modern communication, instrumentation and electronic system. Though today the entire signal processing world seems to be digital, the presence of continuous time filters can be found at every interface with analog world.

With the evolution of several new active building blocks in open literature, there is a great advancement in active filter design. Voltage Differencing Buffered Amplifier (VDBA) is a recently proposed voltage mode active block which has been widely used for filtering as well as signal generation circuits. VDBA combines the features of transconductance amplifier and buffered amplifier.

To meet the stringent sensitivity requirements, higher order analog filters are designed by simulating resistively terminated LC ladder. Active simulation of LC ladder is done primarily by three techniques Element replacement method, Operational simulation and wave active approach. The present work deals with the realization of higher order analog filters using wave active approach. Wave active approach offers several advantages over other techniques such as modular structure, use of lossy integrators etc. This approach is quite similar to the element replacement approach for the design of higher order filters. In this approach wave port terminator equivalents of shunt arm and series arm of a doubly terminated LC ladder are designed with the help of different active building blocks. In the present work, Voltage differencing buffered amplifier has been used to derive series and shunt arm wave equivalents. The workability of the discussed method is shown by implementing 4<sup>th</sup> order Butterworth low pass filter. Due to virtue of wave active approach a complementary high pass output response is also obtained. All the circuits presented in the dissertation have been simulated on PSPICE.

# CONTENTS

<b>Candidate's Declaration</b>	i
<b>Certificate</b>	ii
<b>Acknowledgement</b>	iii
<b>Abstract</b>	iv
<b>Contents</b>	v
<b>List of Figures</b>	ix
<b>List of Tables</b>	xiii
<b>List of Abbreviations</b>	xiv
<b>CHAPTER 1 INTRODUCTION</b>	1
1.1 FUNDAMENTALS OF FILTER	2
1.2 CLASSIFICATION OF FILTERS	3
1.2.1 Classification of filters on the basis of frequency characteristics	3
1.2.2 Classification of filters on the basis of components used in their Implementation	4
1.2.3 Classification of filters on the basis of structures	5
1.3 CHARACTERIZATION OF FILTERS	9
1.3.1 Specifications of First order Filters	9
1.3.2 Specifications of Second order Filters	9
1.3.3 Specifications of Higher order Filters	10

1.4	DESIGN ISSUES IN ACTIVE FILTER DESIGN	11
1.5	ORGANISATION OF THE DISSERTATION	12
	REFERENCES	13
<b>CHAPTER 2 HIGHER ORDER FILTER REALIZATION</b>		14
2.1	CASCADE DESIGN	15
2.2	MULTIPLE LOOP FEEDBACK (MLF) APPROACH	17
2.3	LADDER SIMULATION APPROACH	20
2.3.1	Element Replacement	20
2.3.2	Operational Simulation	25
2.4	CONCLUSION	28
	REFERENCES	29
<b>CHAPTER 3 ACTIVE BLOCKS USED FOR REALIZATION OF WAVE ACTIVE FILTER</b>		30
3.1	OPERATIONAL TRANSCONDUCTANCE AMPLIFIER	30
3.2	CURRENT FEEDBACK OPERATIONAL AMPLIFIER	31
3.3	DIFFERENTIAL VOLTAGE CURRENT CONVEYOR TRANSCONDUCTANCE AMPLIFIER	32
3.4	DIFFERENTIAL VOLTAGE CURRENT CONTROLLED CONVEYOR TRANSCONDUCTANCE AMPLIFIER	33
3.5	CURRENT CONTROLLED DIFFERENTIAL DIFFERENCE CURRENT CONVEYOR TRANSCONDUCTANCE AMPLIFIER	35

3.6	OPERATIONAL TRANSRESISTANCE AMPLIFIER	36
3.7	VOLTAGE DIFFERENCING TRANSCONDUCTANCE AMPLIFIER	37
3.8	CONCLUSION	38
	REFERENCES	39
<b>CHAPTER 4</b>	<b>WAVE ACTIVE FILTER</b>	<b>42</b>
4.1	INTRODUCTION	42
4.2	WAVE ACTIVE FILTER APPROACH	43
4.3	WAVE ACTIVE EQUIVALENTS (WAEs)	46
4.3.1	Wave equivalents for series arm impedance	46
4.3.2	Wave Equivalents for shunt arm admittance	48
4.3.3	Interconnection Rules	49
4.3.4	Wave equivalents for Tuning Circuits	51
4.3.5	Wave Active Filter simulation example	54
4.4	REALIZATION OF WAVE ACTIVE EQUIVALENTS	55
4.5	SALIENT FEATURES OF WAF	56
4.6	IMPLEMENTATION OF WAVE ACTIVE FILTER USING DIFFERENT ACTIVE DEVICES	57
4.6.1	CFOA based Wave Active Filter	57
4.6.2	DVCCTA based Wave Active Filter	58
4.7	CONCLUSION	60
	REFERENCES	61

<b>CHAPTER 5</b>	<b>VDBA BASED WAVE ACTIVE FILTER</b>	<b>62</b>
5.1	INTRODUCTION	62
5.2	VOLTAGE DIFFERENCING BUFFERED AMPLIFIER	62
5.2.1	Circuit Description of the VDBA	63
5.2.2	Characterization of the VDBA	64
5.3	DO-VDBA	68
5.3.1	Circuit Description of the DO-VDBA	68
5.3.2	Characterization of the DO-VDBA	68
5.4	REALIZATION OF WAVE ACTIVE FILTER USING VDBA	72
5.4.1	Wave Equivalent for series arm inductor	72
5.4.1.1	Summer circuit based on VDBA	72
5.4.1.2	Subtractor circuit based on VDBA	74
5.4.1.3	Lossy subtracting integrator circuit by using VDBA	76
5.4.2	Wave equivalent for shunt arm capacitor	79
5.4.3	Realization of 4 <sup>th</sup> order Wave Active Filters	80
5.5	CONCLUSIONS	83
	REFERENCES	84
<b>CHAPTER 6</b>	<b>CONCLUSION AND FUTURE WORK</b>	<b>86</b>
<b>APPENDICES</b>		<b>88</b>

## LIST OF FIGURES

Fig. 1-1 (a) General and (b) practical filter characteristics

Fig. 1-2 Frequency characteristics of all basic filters. Solid lines: ideal characteristics; dashed lines: real characteristics

Fig. 1-3 (a) Passive and (b) Active Realization of a low pass filter.

Fig. 1-4 General form of Second Order Sallen Key Filter.

Fig. 1-5 Op-amp based KHN Biquad Filter (SIMO Structure).

Fig. 1-6 CFOA based universal Filter (MISO Structure).

Fig. 1-7 CDTA based universal Filter (MIMO Structure).

Fig. 1-8 Filter Specification for Low Pass Filter.

Fig. 1-9 Filter Specification for Band Pass Filter.

Fig. 2-1: Different approaches to design higher order active filters.

Fig. 2-2: Cascade connection of biquad sections to realize  $n^{\text{th}}$  order filter.

Fig. 2-3 Multiple Loop Feedback topologies with all pole band pass realization (a) FLF (b) PRB (c) SCF (d) LF (e) MLF (f) IFLF (g) MSF.

Fig. 2-4 General Impedance Converter (GIC) circuit.

Fig. 2-5 Realization of floating inductor based on GIC.

Fig. 2-6 (a) Circuit diagram for GIC (b) symbolic representation of GIC.

Fig. 2-7 Conversion of a Resistance into an Inductor.

Fig. 2-8 (a) LC Ladder network (b) Subnetwork of inductors (c) simulated LC ladder.



Fig.2-9 Circuit for Realization of FDNR with  $D = C^2R$ .

Fig. 2-10 A sixth order LC low pass ladder.

Fig. 2-11 Block diagram representation of Eq. (2.13).

Fig.2-12 Block diagram redrawn from Fig. 2-11.

Fig.2-13 Leapfrog structure of ladder circuit.

Fig. 2-14 Summing lossy integrator (a) inverting configuration (b) non-inverting configuration.

Fig. 3-1 Schematic symbol of Operational Transconductance Amplifier.

Fig. 3-2 CMOS Realization of OTA.

Fig. 3-3 Functional Block Diagram of CFOA.

Fig.3-4 Schematic symbol of DVCCTA.

Fig. 3-5 CMOS Realization of DVCCTA.

Fig. 3-6 Schematic symbol of DVCCCTA.

Fig. 3.7 CMOS Realization of DVCCCTA.

Fig. 3-8 Symbolic representation of CCDDCCTA.

Fig. 3-9 CMOS Realization of CCDDCCTA.

Fig. 3-10 Circuit Symbol of OTRA.

Fig. 3-11 Symbolic diagram of VDTA.

Fig. 3-12 CMOS Realization of VDTA.

Fig. 4-1 A two port network with incident and reflected waves.

Fig.4-2 General form of LC Ladder circuit.

Fig. 4-3 Two port network (a) series arm impedance and (b) shunt arm admittance.

Fig. 4-4 Active realization of (a) Eq. (4.8) and (b) Eq. (4.9).

Fig. 4.5 (a) series arm inductor (b) wave equivalent.

Fig. 4-6 (a) series arm capacitor (b) wave equivalent  $\tau^2$ .

Fig. 4.7 (a) Shunt arm capacitor (b) wave equivalent.

Fig. 4-8(a) Shunt arm inductor (b) wave equivalent.

Fig. 4-9 (a) Two adjacent 2PN (b) their WAEs interconnected.

Fig. 4-10 cross cascade connection of two adjacent WAEs with equal normalization resistances.

Fig. 4-11 Wave flow diagram for (a) series tuned series arm circuit (b) parallel tuned shunt arm circuit.

Fig. 4-12 Wave flow diagram for parallel tuned series arm circuit.

Fig. 4-13 Wave flow diagram for series tuned shunt arm circuit.

Fig. 4-14(a) 3<sup>rd</sup> order LC lowpass prototype (b) its wave equivalent.

Fig. 4-15 Wave equivalent of series inductor based on CFOA.

Fig. 4-16 Simulation Result for CFOA based Wave Active LPF and HPF

Fig. 4-17 Wave equivalent of series arm inductor by using DVCCTA.

Fig. 4-18 Simulation Result for DVCCTA based Wave Active LPF and HPF

Fig. 5-1 Circuit symbol of VDBA.

Fig. 5-2 CMOS Realization of VDBA.

Fig. 5-3 DC Characteristics of VDBA (a) Input characteristics (b) Output characteristics.

Fig. 5-4 AC characteristics of VDBA (a) Input Characteristics (b) Output characteristics.

Fig. 5-5 Symbolic representation for DO-VDBA.

Fig. 5-6 CMOS Realization of DO-VDBA.

Fig. 5-7 DC Characteristics of DO-VDBA (a) Input Characteristics (b) Output Characteristics

Fig. 5-8 AC Characteristics of DO-VDBA (a) Input Characteristics (b) Output Characteristics

Fig. 5-9 Summer circuit using DO-VDBA.

Fig. 5-10 Simulation Result of DO-VDBA based Summer circuit.

Fig. 5-11 Subtractor circuit using DO-VDBA.

Fig. 5-12 Simulation Result of DO-VDBA based subtractor.

Fig. 5-13 Simulation Result of DO-VDBA based Inverter circuit.

Fig. 5-14 Lossy integrator based on DO-VDBA.

Fig. 5-15 Simulation Result of Lossy integrator (a) AC response (b) Transient Response

Fig. 5-16 Realization of Wave Equivalent for Series Inductor Using DO-VDBA.

Fig. 5.17 Realization of wave equivalent for shunt capacitor using DO-VDBA.

Fig. 5-18 4<sup>th</sup> order LC ladder low pass prototype.

Fig. 5-19 Wave equivalent of low pass prototype.

Fig. 5-20 Frequency Response Low Pass Filter.

Fig. 5-21 Transient response of Low Pass Filter.

Fig. 5-22 Frequency Response of High Pass Filter.

Fig. 5-23 Transient Response of High Pass Filter.

## **LIST OF TABLES**

Table 4-1 Wave flow diagrams for series arm impedances.

Table 4-2 Wave flow diagrams for shunt arm admittances.

Table 5-1 Aspect ratio for realization of VDBA

Table 5-2 Aspect ratio for realization of DO-VDBA

## LIST OF SYMBOLS, ABBREVIATIONS

S.No.	Symbols/ Abbreviations	Descriptions
1.	$g_m$	Transconductance
2.	$\omega_0$	Natural Frequency
3.	$Q$	Quality Factor
4.	$V_{SS}$	Source Supply Voltage
5.	$V_{DD}$	Drain Supply Voltage
6.	$R_m$	Trans-resistance
7.	$I_B$	Bias Current
8.	ASP	Analog Signal Processing
9.	DSP	Digital Signal Processing
10.	ABB	Active Building Block
11.	CMOS	Complementary Metal Oxide Semiconductor
12.	Op-Amp	Operational Amplifier
13.	VDBA	Voltage Differencing Buffered Amplifier
14.	FDNR	Frequency Dependent Negative Resistance
15.	OTA	Operational Transconductance Amplifier
16.	CC	Current Conveyor
17.	CFOA	Current Feedback Operational Amplifier
18.	CDBA	Current Differencing Buffer Amplifier

19.	DVCCTA	Differential Voltage Current Conveyor Transconductance Amplifier
20.	DVCCCTA	Differential Voltage Current Controlled Conveyor Transconductance Amplifier
21.	CCDDCCTA	Current Controlled Differential Difference Current Conveyor Transconductance Amplifier
22.	OTRA	Operational Trans-Resistance Amplifier
23.	VDTA	Voltage Differencing Transconductance Amplifier
24.	WAF	Wave Active Filter
25.	WAE	Wave Active Equivalent
26.	VD-DIBA	Voltage Differencing Differential Input Buffered Amplifier
27.	DO-VDBA	Dual output Voltage Differencing Buffered Amplifier
28.	FB-VDBA	Fully Balanced Voltage Differencing Buffered Amplifier