

BLACKOUT SIMULATION OF A POWER GRID
DISSERTATION

SUBMITTED IN PARTIAL FULFILLMENT OF THE REQUIREMENTS

FOR THE AWARD OF THE DEGREE

OF

MASTER OF TECHNOLOGY

IN

ELECTRICAL ENGINEERING (POWER SYSTEMS)

Submitted by:

RAHUL SINGH

(2K13/ PSY/ 12)

Under the supervision of

Dr. S.T.NAGARAJAN



DEPARTMENT OF ELECTRICAL ENGINEERING

DELHI TECHNOLOGICAL UNIVERSITY

(Formerly Delhi College of Engineering)

Bawana Road, Delhi-110042

2015