



**DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
DELHI TECHNOLOGICAL UNIVERSITY**

CERTIFICATE

Certified that the project entitled “**IMPLEMENTING PARALLEL PSO ALGORITHM USING MAPREDUCE ARCHITECTURE**” submitted by **PRATEEK KUMAR VARSHNEY [2K13/SWE/11]** in the partial fulfillment of the requirements for the award of the degree of **Master of Technology** (Software Engineering) of Delhi Technological University, is a record of students’ own work carried under our supervision and guidance. The project report embodies results of original work and studies carried out by students and the contents do not form the basis for the award of any other degree to the candidate or to anybody else.

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