MAJOR PROJECT REPORT

On

IMPLEMENTING PARALLEL PSO ALGORITHM USING MAPREDUCE ARCHITECTURE

Submitted in partial fulfillment of the requirements for the award of the degree

of

MASTER OF TECHNOLOGY

In

COMPUTER SCIENCE AND ENGINEERING

Submitted by

PRATEEK KUMAR VARSHNEY (2K13/SWE/11)

Under the guidance of

Dr. KAPIL SHARMA



DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
DELHI TECHNOLOGICAL UNIVERSITY, DELHI
2013-15