

A DISSERTATION ON  
**A NEW REALIZATION OF LINEAR PHASE  
DETECTOR**

*SUBMITTED IN PARTIAL FULFILLMENT OF THE REQUIREMENT  
FOR THE AWARD OF THE DEGREE OF  
MASTER OF TECHNOLOGY  
(VLSI & EMBEDDED SYSTEM DESIGN)*

Submitted By:  
**RAJEEV RANJAN**  
UNIVERSITY ROLL NO. -2K11/VLS/20

Under the esteemed Guidance of:  
**Ms. N. JAYANTHI**



**DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
DELHI TECHNOLOGICAL UNIVERSITY  
2011-2013**